

MATERIALS & SAFETY - R&D

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FORM C

TYPE TEST VERIFICATION REPORT

All Micro-generators connected to the **DNO Distribution Network** shall be **Fully Type Tested**. This form is the **Manufacturer**'s declaration of compliance with the requirements of G98.

This form should be used when making a Type Test submission to the Energy Networks Association (ENA).

If the Micro-generator is Fully Type Tested and already registered with the ENA Type Test Verification Report Register, the Installation Document should include the Manufacturer's Reference Number (the Product ID), and this form does not need to be submitted.

Where the Micro-generator is Fully Type Tested and not registered with the ENA Type Test Verification Report Register this form needs to be completed and provided to the DNO, to confirm that the Micro-generator has been tested to satisfy the requirements of this EREC G98.

Manufacturer's reference number		Fronius Symo GEN24 6.0 / Fronius Symo GEN24 6.0 Plus / Fronius Symo GEN24 6.0 Lite					
Micro-generato	r technol	ogy	trans	transformerless			
Manufacturer r	name		Fronius International GmbH				
Address				nter Fronius Str 1 Wels-Thalheim, 7	Austria		
Tel	+43-724	2-241-0		Fax	+43-7242-241-224		
E:mail	pv@fron	ius.com		Web site	www.fronius.com		
			Connection Option				
Registered Ca	pacity.	<u></u>	kW single phase, single, split or three phase system				
use separate sh more than one		6	kW three phase				
connection option.		_	kW two phases in three phase system				
			kW tv	wo phases split ph	ase system		
Manufacturer 1	Type Test	declaration - L certif	v that	all products suppl	ied by the company with the above		

Manufacturer Type Test declaration. - I certify that all products supplied by the company with the above **Fully Type Tested** reference number will be manufactured and tested to ensure that they perform as stated in this document, prior to shipment to site and that no site modifications are required to ensure that the product meets all the requirements of EREC G98.

Signed Günter Fronty St. A-4600 Wels/Thalheim On behalf of Tel: 443 / (1) A-4600 Wels/

Note that testing can be done by the **Manufacturer** of an individual component or by an external test house.

Where parts of the testing are carried out by persons or organisations other than the **Manufacturer** then that person or organisation shall keep copies of all test records and results supplied to them to verify that the testing has been carried out by people with sufficient technical competency to carry out the tests.



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Operating Range: This test should be carried out as specified in EN 50438 D.3.1.

Active Power shall be recorded every second. The tests will verify that the Micro-generator can operate within the required ranges for the specified period of time.

The Interface Protection shall be disabled during the tests.

In case of a PV Micro-generator the PV primary source may be replaced by a DC source.

In case of a full converter **Micro-generator** (e.g. wind) the primary source and the prime mover **Inverter**/rectifier may be replaced by a **DC** source.

In case of a DFIG **Micro-generator** the mechanical drive system may be replaced by a test bench motor.

Test 1

Voltage = 85% of nominal (195.5 V)

Frequency = 47.5 Hz

Power factor = 1

Period of test 90 minutes

Test 2

Voltage = 110% of nominal (253 V),

Frequency = 51.5 Hz

Power factor = 1

Period of test 90 minutes

Test 3

Voltage = 110% of nominal (253 V).

Frequency = 52.0 Hz

Power factor = 1

Period of test 15 minutes

Remark: During the tests 1, 2 and 3 the unit does not disconnect, tests have been passed.



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Power Quality – Harmonics: These tests should be carried out as specified in BS EN 61000-3-2. The chosen test should be undertaken with a fixed source of energy at two power levels a) between 45 and 55% and b) at 100% of Registered Capacity. The test requirements are specified in Annex A1 A.1.3.1 (Inverter connected) or Annex A2 A.2.3.1 (Synchronous).

Micro-ger	nerator rating p		ator tested to B	kW		
inicio-gei	retator rating p	ci pilase (ipp)	3,555	KVV		
Harmonic		of Registered acity		Registered acity		
	Measured Value MV in Amps		Measured Value MV in Amps		Limit in BS EN 61000- 3-2 in Amps	Higher limit for odd harmonics 21 and above
2	0.005	0.009	0.009	0.016	1.080	
3	0.016	0.029	0.016	0.028	2.300	
4	0.003	0.006	0.004	0.007	0.430	
5	0.102	0.178	0.125	0.219	1.140	
6	0.002	0.003	0.002	0.004	0.300	
7	0.054	0.095	0.085	0.149	0.770	
8	0.003	0.005	0.002	0.004	0.230	
9	0.006	0.011	0.007	0.012	0.400	
10	0.002	0.003	0.002	0.004	0.184	
11	0.009	0.017	0.047	0.082	0.330	
12	0.001	0.002	0.002	0.004	0.153	
13	0.006	0.011	0.036	0.063	0.210	432 41 4
14	0.002	0.003	0.004	0.007	0.131	
15	0.003	0.006	0.012	0.021	0.150	
16	0.002	0.003	0.004	0.007	0.115	
17	0.014	0.024	0.024	0.042	0.132	
18	0.001	0.002	0.002	0.004	0.102	
19	0.017	0.030	0.018	0.032	0.118	
20	0.002	0.003	0.002	0.004	0.092	
21	0.004	0.008	0.004	0.007	0.107	0.160

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22	0.002	0.003	0.002	0.004	0.084	
23	0.016	0.029	0.012	0.021	0.098	0.147
24	0.004	0.008	0.004	0.007	0.077	The state of
25	0.016	0.029	0.009	0.016	0.090	0.135
26	0.002	0.003	0.002	0.004	0.071	
27	0.003	0.005	0.003	0.005	0.083	0.124
28	0.021	0.036	0.018	0.032	0.066	
29	0.011	0.020	0.007	0.012	0.078	0.117
30	0.027	0.047	0.021	0.037	0.061	
31	0.008	0.014	0.006	0.011	0.073	0.109
32	0.003	0.006	0.003	0.005	0.058	
33	0.004	0.008	0.004	0.007	0.068	0.102
34	0.010	0.018	0.009	0.016	0.054	
35	0.017	0.030	0.007	0.012	0.064	0.096
36	0.004	0.008	0.009	0.016	0.051	
37	0.021	0.036	0.007	0.012	0.061	0.091
38	0.003	0.006	0.027	0.047	0.048	
39	0.004	0.008	0.004	0.007	0.058	0.087
40	0.003	0.005	0.005	0.009	0.046	THE PARK OF ME

Note the higher limits for odd harmonics 21 and above are only allowable under certain conditions, if these higher limits are utilised please state the exemption used as detailed in part 6.2.3.4 of BS EN 61000-3-2 in the box below.



Impedance

ENA Engineering Recommendation G98 Issue 1 – Amendment 4 2019

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Power Quality - Voltage fluctuations and Flicker: These tests should be undertaken in accordance with EREC G98 Annex A1 A.1.3.3 (Inverter connected) or Annex A2 A.2.3.3 (Synchronous). Starting Running Stopping d_{max} d_(t) $d_{(t)}$ P_{II} 2 hours ď d_{max} 0.137 Measured 0% 0% 0.68% 0.67% 0% 0.09 Values at test impedance Normalised to standard impedance Normalised to required maximum impedance 4% 3.3% 4% 3.3% 1.0 3.3% 3.3% 0.65 Limits set under BS EN 61000-3-11 Χ 0.15 Test R 0.24 Ω Ω Impedance R 0.24 * X 0.15 * Standard Ω Ω Impedance 0.4^ 0.25^ Maximum R X Ω Ω

For voltage change and flicker measurements the following formula is to be used to convert the measured values to the normalised values where the power factor of the generation output is 0.98 or above.

Normalised value = Measured value*reference source resistance/measured source resistance at test point.

Single phase units reference source resistance is 0.4 Ω

Two phase units in a three phase system reference source resistance is $0.4~\Omega$.

Two phase units in a split phase system reference source resistance is $0.24~\Omega$.

Three phase units reference source resistance is 0.24Ω .

Where the power factor of the output is under 0.98 then the X to R ratio of the test impedance should be close to that of the Standard Impedance.

The stopping test should be a trip from full load operation.

The duration of these tests need to conform to the particular requirements set out in the testing notes for the technology under test. Dates and location of the test need to be noted below.

Test start	20.01.2020	Test end	14.04.2020	
Test location		chnology GmbH Giefinggasse 2 , Fronius International GmbH, G		

^{*} Applies to three phase and split single phase **Micro-generators**.

[^] Applies to single phase **Micro-generators** and **Micro-generators** using two phases on a three phase system.



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Power quality - DC injection: This test should be carried out in accordance with EN 50438 Annex D.3.10									
Test power level	20%	50%	75%	100%					
Recorded value in Amps	0,0074	0,0068	0,0043	0,0027					
as % of rated AC current	0,085	0,078	0,049	0,031					
Limit	0.25%	0.25%	0.25%	0.25%					

Power Quality – Power factor : This test shall be carried out in accordance with EN 50538 Annex D.3.4.1 but with nominal voltage -6% and +10%. Voltage to be maintained within ±1.5% of the stated level during the test.										
	216.2 V 230 V 253 V									
20% of Registered Capacity	1.00	1.00	1.00							
50% of Registered Capacity	1.00	1.00	1.00							
75% of Registered Capacity	1.00	1.00	1.00							
100% of Registered Capacity	1.00	1.00	1.00							
Limit	>0.95	>0.95	>0.95							



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Protection – Frequency tests: These tests should be carried out in accordance with EN 50438 Annex D.2.4 and the notes in EREC G98 Annex A1 A.1.2.3 (Inverter connected) or Annex A2 A.2.2.3 (Synchronous)

Function	Setting		Trip test		"No trip tests"	
	Frequency	Time delay	Frequency	Time delay	Frequency /time	Confirm no trip
U/F stage 1	47.5Hz	20s	47.50Hz	20.047s	47.7 Hz 30 s	Confirmed
U/F stage 2	47Hz	0.5s	47.00Hz	0.546	47.2 Hz 19.5 s	Confirmed
					46.8 Hz 0.45 s	Confirmed
O/F stage 1	52Hz	0.5s	52.009Hz	0.546s	51.8 Hz 120.0 s	Confirmed
					52.2 Hz 0.45 s	Confirmed

Note. For frequency trip tests the frequency required to trip is the setting \pm 0.1 Hz. In order to measure the time delay a larger deviation than the minimum required to operate the projection can be used. The "No trip tests" need to be carried out at the setting \pm 0.2 Hz and for the relevant times as shown in the table above to ensure that the protection will not trip in error.

Protection – Voltage tests: These tests should be carried out in accordance with EN 50438 Annex D.2.3 and the notes in EREC G98 Annex A1 A.1.2.2 (**Inverter** connected) or Annex A2 A.2.2.2 (Synchronous)

OI AIIIICA A	Z A.Z.Z.Z (S	ynchionous	1				
Function	Setting		Trip test		"No trip tes	"No trip tests"	
	Voltage	Time delay	Voltage	Time delay	Voltage /time	Confirm no trip	
U/V	184V	2.5s	184.02V	2.523	188 V 5.0 s	Confirmed	
					180 V 2.45 s	Confirmed	
O/V stage 1	262.2V	1.0s	261.99V	1.028s	258.2 V 5.0 s	Confirmed	
O/V stage 2	273.7V	0.5s	273.38V	0.531s	269.7 V 0.95 s	Confirmed	
					277.7 V 0.45 s	Confirmed	

Note for Voltage tests the Voltage required to trip is the setting ± 3.45 V. The time delay can be measured at a larger deviation than the minimum required to operate the protection. The No trip tests need to be carried out at the setting ± 4 V and for the relevant times as shown in the table above to ensure that the protection will not trip in error.



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Protection - Lo						
BS EN 62116.				accordance	e with EN 5	0438 Annex
D.2.5 at 10%, 5					The state of	
To be carried out a						
Test Power	10%	55%	100%	10%	55%	100%
Balancing load on islanded network	95% of Registered Capacity	95% of Registered Capacity	95% of Registered Capacity	105% of Registered Capacity	105% of Registered Capacity	105% of Registered Capacity
Trip time. Limit is 0.5 seconds						
For Multi phase	_				down corre	ctly after the
removal of a sing					I ===:	1000/
Test Power	10%	55%	100%	10%	55%	100%
Balancing load on islanded network	95% of Registered Capacity	95% of Registered Capacity	95% of Registered Capacity	105% of Registered Capacity	105% of Registered Capacity	105% of Registered Capacity
Trip time. Ph1						
fuse removed						
Test Power	10%	55%	100%	10%	55%	100%
Balancing load on islanded network	95% of Registered Capacity	95% of Registered Capacity	95% of Registered Capacity	105% of Registered Capacity	105% of Registered Capacity	105% of Registered Capacity
Trip time. Ph2			###			(iee
fuse removed						
Test Power	10%	55%	100%	10%	55%	100%
Balancing load on islanded network	95% of Registered Capacity	95% of Registered Capacity	95% of Registered Capacity	105% of Registered Capacity	105% of Registered Capacity	105% of Registered Capacity
Trip time. Ph3		24.			24	#41
fuse removed						
Note for technologestablishing that the 1.0 s for these technologies.	e trip occurred					
Indicate additiona	ıl shut down t	ime included	in above resu	ults.		ms
For Inverters test	sted to BS EN	N 62116 the	following sub	set of tests	should be re	corded in the
Test Power and	33%	66%	100%	33%	66%	100%
imbalance	-5% Q	-5% Q	-5% P	+5% Q	+5% Q	+5% P
	Test 22	Test 12	Test 5	Test 31	Test 21	Test 10
Trip Time. Limit is 0.5s	186.4 ms	163.6 ms	404.7 ms	208.4 ms	169.9 ms	418.7 ms



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Protection – Frequency change, Vector Shift Stability test: This test should be carried out in accordance with EREC G98 Annex A1 A.1.2.6 (Inverter connected) or Annex A2 A.2.2.6 (Synchronous).								
	Start Frequency	Change	Confirm no trip					
Positive Vector Shift	49.0Hz	+50 degrees	Confirmed					
Negative Vector Shift	50.0Hz	-50 degrees	Confirmed					

Protection – Frequency change, RoCoF Stability test: The requirement is specified in section 11.3, test procedure in Annex A.1.2.6 (Inverter connected) or Annex A2 A.2.2.6 (Synchronous).Ramp rangeTest frequency ramp:Test DurationConfirm no trip49.0 Hz to 51.0Hz+0.95 Hzs-12.1 sConfirmed51.0 Hz to 49.0Hz-0.95 Hzs-12.1 sConfirmed

Limited Frequency Sensitive Mode - Overfrequency test: This test should be

carried out in accordance with EN 50438 Annex D.3.3 Power response to overfrequency. The test should be carried out using the specific threshold frequency of 50.4 Hz and **Droop** of 10%. Test sequence at Primary Power Source Measured Frequency Active **Registered Capacity** Power **Active Power** Gradient >80% Output Step a) 50.00 Hz ±0.01 Hz 6030W 50.00Hz Step b) 50.45 Hz ±0.05 Hz 5960W 50.45Hz Step c) 50.70 Hz ±0.10 Hz 5660W 50.70Hz Step d) 51.15 Hz ±0.05 Hz 5115W 51.15Hz 6.1kW 20%/Hz Step e) 50.70 Hz ±0.10 Hz 5660W 50.70Hz Step f) 50.45 Hz ±0.05 Hz 5960W 50.45Hz 6030W Step g) 50.00 Hz ±0.01 Hz 50.00Hz Test sequence at Primary Power Source Active Measured Frequency Registered Capacity 40% **Active Power** Power Gradient - 60% Output Step a) 50.00 Hz ±0.01 Hz 3040W 50.00Hz Step b) 50.45 Hz ±0.05 Hz 2995W 50.45Hz Step c) 50.70 Hz ±0.10 Hz 2840W 50.70Hz Step d) 51.15 Hz ±0.05 Hz 2570W 51.15Hz 3.1kW 20%/Hz Step e) 50.70 Hz ±0.10 Hz 2840W 50.70Hz Step f) 50.45 Hz ±0.05 Hz 2995W 50.45Hz Step g) 50.00 Hz ±0.01 Hz 3040W 50.00Hz Steps as defined in EN 50438

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Test sequence Measured Active Frequency Primary power source							
Test a) 50 Hz ± 0.01 Hz	6000W	50Hz	6.12kW				
Test b) Point between 49.5 Hz and 49.6 Hz	6000W	49.55Hz	6.12kW				
Test c) Point between 47.5 Hz and 47.6 Hz	6000W	47.55Hz	6.12kW				

Re-connection timer.									
Test should prove that the reconnection sequence starts after a minimum delay of 20 s for									
restoration of voltage and frequency to within the stage 1 settings of Table 2.									
Time delay	Measured		Checks on no reconnection when voltage or frequency is						
setting	delay		brought to just	outside stage 1	limits of table 2.				
20.0s	71.92s		At 266.2V At 180.0V At 47.4Hz At 52.1Hz						
Confirmation that the Micro-generator			Confirmed	Confirmed	Confirmed	Confirmed			
does not re-co	onnect.								

Fault level contribution	: These	tests shall	be carried	out in acc	cordance with EREC	
G98 Annex A1 A.1.3.5 (In	nverter co	nnected)	and Annex A	2 A.2.3.4	(Synchronous).	
For machines with electro-magnetic output			For Inverte	For Inverter output		
Parameter	Symbol	Value	Time after fault	Volts	Amps	
Peak Short Circuit current	i	17 41- 1	20ms	4.24	49.4	
Initial Value of aperiodic current	A	Water	100ms	3.6	22.4	
Initial symmetrical short- circuit current*	l _k	0.	250ms	3.43	14.3	
Decaying (aperiodic) component of short circuit current*	i _{DC}	Case	500ms	3.4	10.3	
Reactance/Resistance Ratio of source*	×/ _R	(4 =)	Time to trip	0.110	In seconds	

For rotating machines and linear piston machines the test should produce a 0 s - 2 s plot of the short circuit current as seen at the **Micro-generator** terminals.

^{*} Values for these parameters should be provided where the short circuit duration is sufficiently long to enable interpolation of the plot

Logic Interface.	Yes
Self-Monitoring solid state switching: No specified test requirements. Refer to EREC G98 Annex A1 A.1.3.6 (Inverter connected).	NA
It has been verified that in the event of the solid state switching device failing to disconnect the Micro-generator , the voltage on the output side of the switching device is reduced to a value below 50 V within 0.5 s.	

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Additional comments		